



Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	3.2m Ω @10V	95A
	4.0m Ω @4.5V	

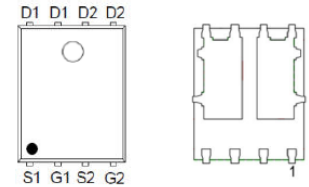
Feature

- Split Gate Trench Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

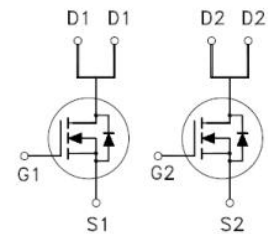
Application

- Power Management
- Load Switching

PDFN5X6-8L



Schematic diagram



Package Marking and Ordering Information

Part Number	Package	Marking	Packing	Reel Size	Tape Width	Qty
GPT032ND04LND	PDFN5X6-8L	T032ND04L	Reel & Tape	330mm	12mm	5000pcs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	$T_C = 25^\circ\text{C}$	95
		$T_C = 100^\circ\text{C}$	60
Pulsed Drain Current ²	I_{DM}	380	A
Single Pulsed Avalanche Current ³	I_{AS}	29	A
Single Pulsed Avalanche Energy ³	E_{AS}	210	mJ
Power Dissipation ⁵	P_D	63	W
Thermal Resistance from Junction to Ambient ⁶	$R_{\theta JA}$	60	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~ +150	$^\circ\text{C}$

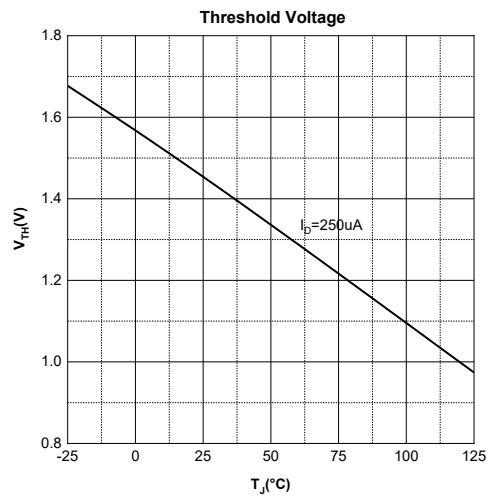
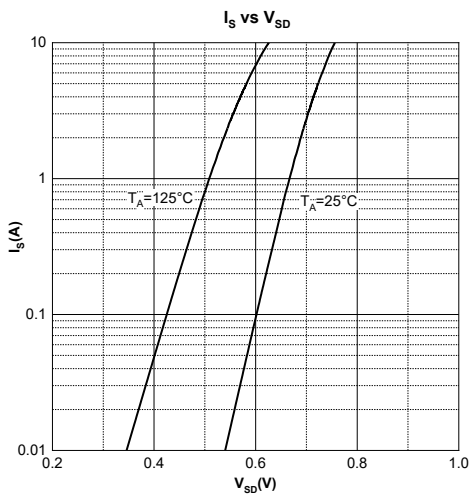
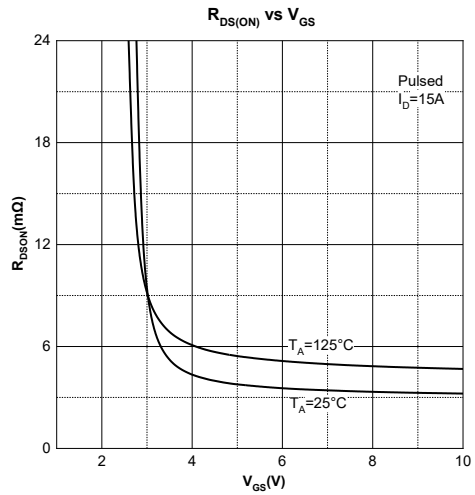
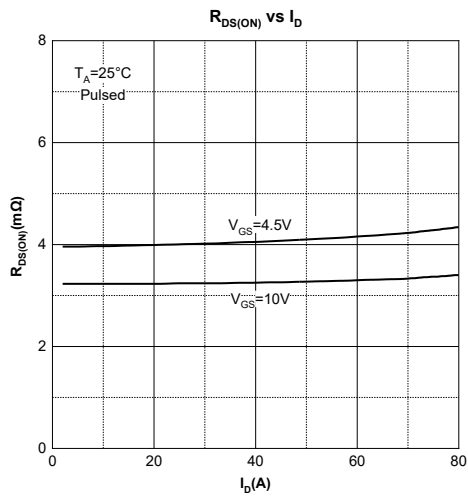
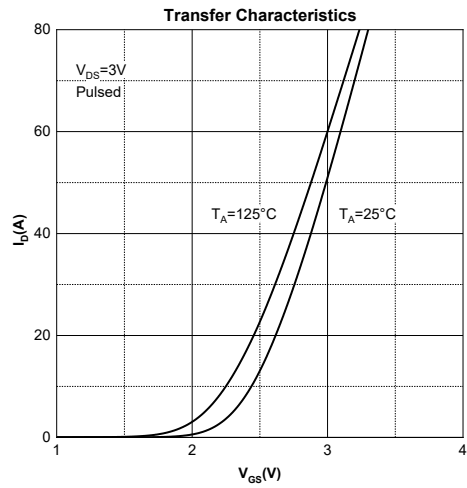
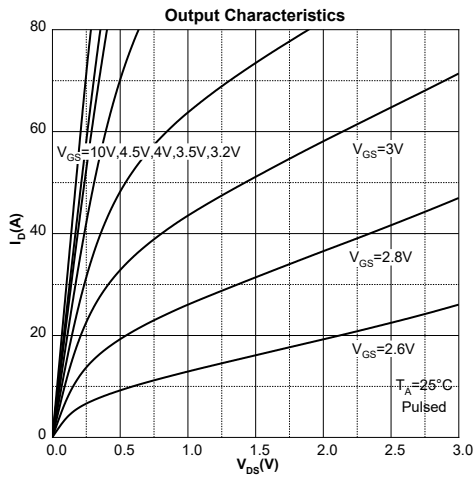
MOSFET ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

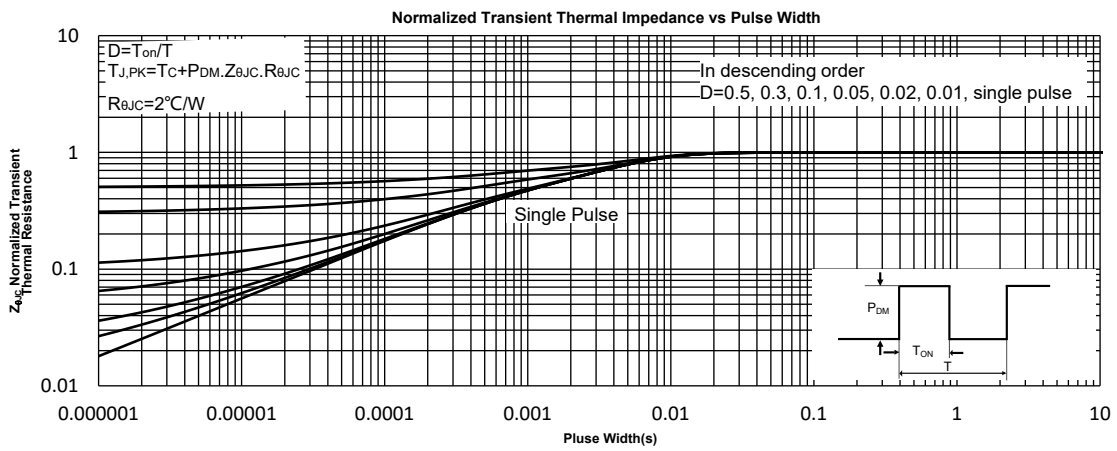
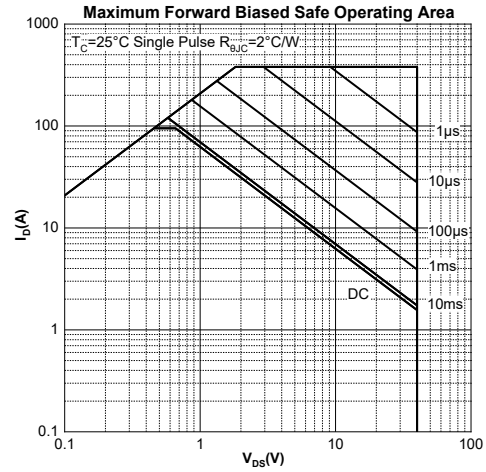
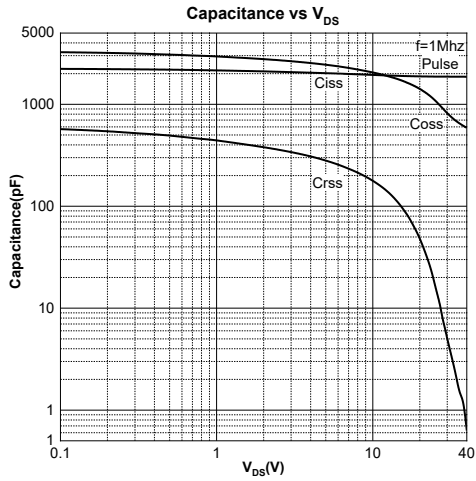
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40V, V_{GS} = 0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
On Characteristics⁴						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 15A$		3.2	4.2	m Ω
		$V_{GS} = 4.5V, I_D = 10A$		4.0	6.0	
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 20V, V_{GS} = 0V, f = 1MHz$		1888		pF
Output Capacitance	C_{oss}			1372		
Reverse Transfer Capacitance	C_{rss}			41		
Gate Resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		2.2		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = 25V, V_{GS} = 10V, I_D = 15A$		32		nC
Gate-Source Charge	Q_{gs}			5		
Gate-Drain Charge	Q_{gd}			6		
Gate Plateau Voltage	$V_{plateau}$			2.7		V
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20V, V_{GS} = 10V, R_G = 1.6\Omega, I_D = 50A$		15		ns
Turn-On Rise Time	t_r			6		
Turn-Off Delay Time	$t_{d(off)}$			44		
Turn-Off Fall Time	t_f			12		
Source-Drain Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$V_{GS} = 0V, I_S = 15A$			1.2	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 50A, dI/dt = 100A/\mu s$		68		ns
Diode Reverse Recovery Charge	Q_{rr}				15	

Notes:

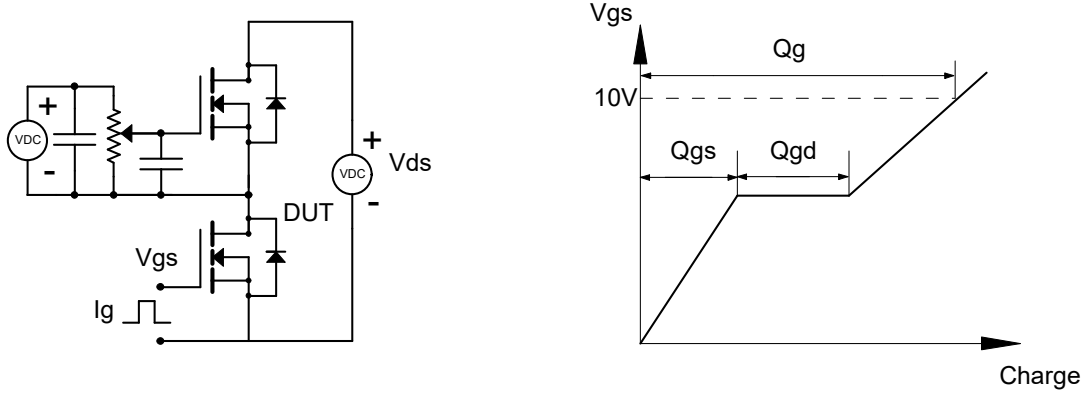
1. The maximum current rating is limited by package. And device mounted on a large heatsink.
2. Pulse Test: Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
3. E_{AS} condition: $V_{DD} = 40V, V_{GS} = 10V, L = 0.5mH, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
4. Pulse Test: Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. The power dissipation P_D is limited by $T_{J(MAX)} = 150^\circ\text{C}$. And device mounted on a large heatsink.
6. Device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics

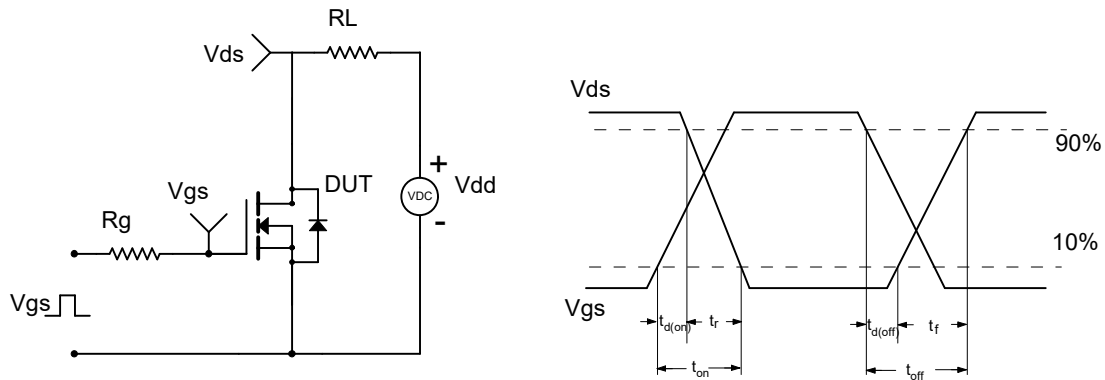




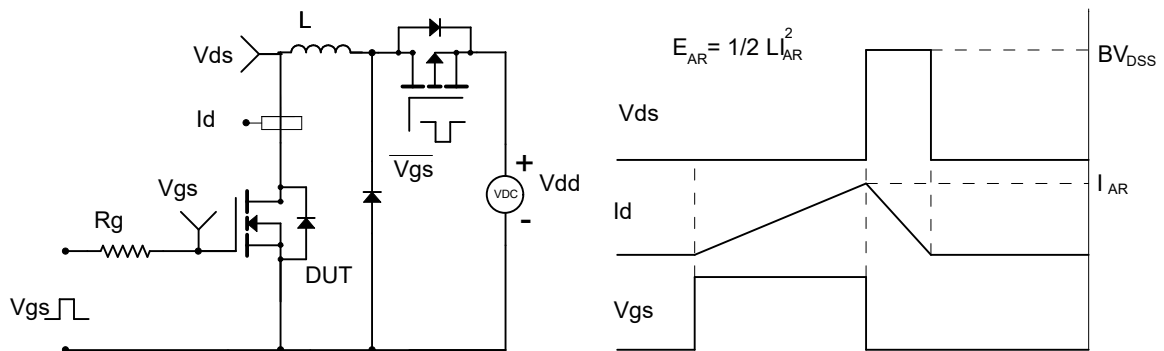
Gate Charge Test Circuit & Waveform



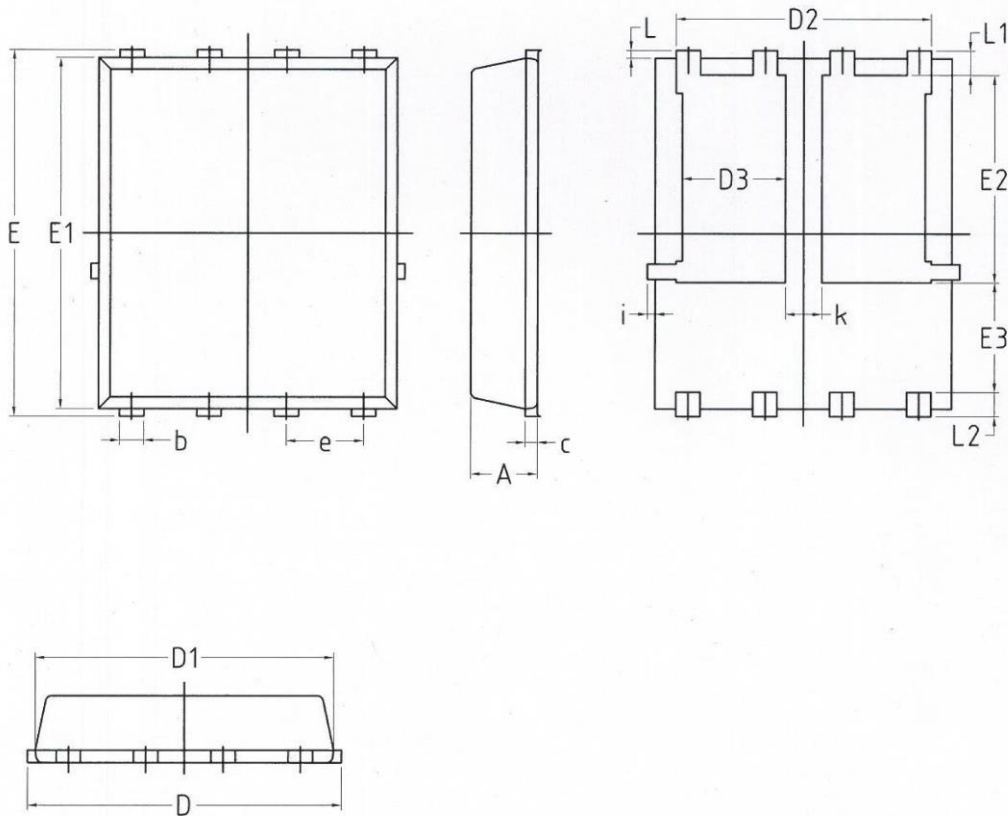
Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



PDFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.200	0.035	0.047
b	0.340	0.500	0.013	0.020
c	0.203 BSC		0.008 BSC	
D	4.800	5.400	0.189	0.213
D1	4.800	5.000	0.189	0.197
D2	4.110	4.310	0.162	0.170
D3	1.500	1.800	0.059	0.071
E	5.950	6.350	0.234	0.250
E1	5.600	5.900	0.220	0.232
E2	3.300	3.700	0.130	0.146
E3	1.400	-	0.055	-
e	1.270 BSC		0.050 BSC	
L	0.050	0.250	0.002	0.010
L1	0.380	0.680	0.015	0.027
L2	0.380	0.840	0.015	0.033
i	-	0.180	-	0.007
k	0.500	0.700	0.020	0.028

Attention:

- GreenPower Electronics reserves the right to improve product design function and reliability without notice.
- Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.
- GreenPower Electronics products belong to consumer electronics or other civilian electronic products.